

ABSTRACT

By executing a storage control program, a CPU core stores in a time series detection signals from sensors, operation signals outputted to operating members, and error
5 detection results in a ring buffer of a RAM whenever a change occurs in any of these data. The CPU core outputs the data stored in the ring buffer to a personal computer, for example, based on an output program. Accordingly, an operator can view the history of detection signals outputted
10 from the sensors and the operation signals outputted to the operating members up to the point that the error is generated on the CRT screen of the personal computer. The operator can easily analyze the cause of the generated error.